

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E. AD 102 or Ans COPIED	PATENT DATE
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APPLICATION NO. 09/960517	CONT/PRIOR F	CLASS 438	SUBCLASS 257	ART UNIT 2813 2822	EXAMINER Thomas
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[illegible]

Hitoshi Aoki

Semiconductor device and fabrication process therefor

PTO-2040
12/89

ISSUING CLASSIFICATION			
CLASS		SUBCLASS (ONE SUBCLASS PER BLOCK)	
INTERNATIONAL CLASSIFICATION			

☐ Continued on verso Slip (include Slip Control)

<p>INVENTORIAL</p> <p>DESCRIPTION</p> <th data-bbox="704 1249 1096 1362"> <p>DRAWINGS</p> <p>Fig. 1 Fig. 2 Fig. 3</p> <th data-bbox="1096 1249 1424 1362"> <p>CLASSIFICATION</p> </th></th>	<p>DRAWINGS</p> <p>Fig. 1 Fig. 2 Fig. 3</p> <th data-bbox="1096 1249 1424 1362"> <p>CLASSIFICATION</p> </th>	<p>CLASSIFICATION</p>
<p><input type="checkbox"/> The term of this patent is extended to _____ (date) has been determined.</p>	<p>(Priority Extension) _____ (Date) _____</p>	<p>NOTES OF EXTENSION</p>
<p><input type="checkbox"/> The term of this patent shall be _____ months of this Patent No. _____</p>	<p>(Priority Extension) _____ (Date) _____</p>	<p>Notes for</p> <p>Amount Due _____</p>
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